Author Correction

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Author Correction: Facile integration of electro-optic SiO₂/ITO heterointerfaces in MIS structures for CMOS-compatible plasmonic waveguide modulation

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In the originally published version of this article¹, references 83–92 were inadvertently included in the reference list due to a glitch in the LaTeX template used. These references have been removed from the corrected version of the article.

We apologize for any inconvenience this may have caused.

The original paper has been updated.

References

1. Alfaraj, N. et al. Facile integration of electro-optic SiO2/ITO heterointerfaces in MIS structures for CMOS-compatible plasmonic waveguide modulation. *Light: Advanced Manufacturing* **4**, 34 (2023).

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